Sirindhorn International Institute of Technology

Thammasat University at Rangsit

School of Information, Computer and Communication Technology

ECS 371: Problem Set 10

Semester/Year: **1/2009**
Course Title: Digital Circuits
Instructor: Dr. Prapun Suksompong (prapun@siit.tu.ac.th)
Course Web Site: http://www.siit.tu.ac.th/prapun/ecs371/

Due date: September 23, 2009 (Wednesday)

Instructions

1. Part A will be read in detail. Your answer does not affect your HW score. However, it will have significant effect on your participation score which is much more than this HW score.
2. ONE question from part B will be graded. Of course, you do not know which problems will be selected; so you should work on all of them.
3. Late submission will not be accepted.
4. **Write down all the steps** that you have done to obtain your answers. You may not get full credit even when your answer is correct without showing how you get your answer.

Part A

The class participation score for this class is judged from how much you **actively** participate in the class discussion both inside and outside of the classroom.

Please honestly answer the following questions. Your answer may influence the actual score that you get.

1. How many times have you been absent from the class? Are there any specific reason(s)? Please explain.
2. How many times have you been late (> 7 mins) for the class? Are there any specific reason(s)? Please explain.

3. How many times have you left the class early (> 7 mins)? Are there any specific reason(s)? Please explain.
4. How many times have you correctly informed the instructors the typo or mistake on the whiteboard/slides/hw/etc? Please provide some short description about each of the issues.
5. How many times have you discussed with the instructor about the class? (Ask questions, express concerns, etc.)
6. On the scale of 10, what score should you get for the class participation? Please explain.
7. Are there any obstacles that prevent you from learning the material presented in this class?

Part B:

The questions in part B are assigned from the following textbook:

Thomas L. Floyd, *Digital Fundamentals*, 10th Edition, Pearson Education International (2009).

Please submit your solutions for the following questions

- Chapter 9: **1, 2, 3, 4, 6**
    - For Q4, assume that the **LSB** is applied first.
- Chapter 10: **1, 2, 3, 9, 10, 12**
    - For Q10, recall that DRAM use address multiplexing.