# School of Information, Computer and Communication Technology Sirindhorn International Institute of Technology Thammasat University 

## Quiz 8 Sample

Rules: Closed book. Closed notes. No Calculator. Do not cheat. Do not panic.

a) Fill in the blank: Theoretically, the output voltage of an inverting integrator shown above is given by

$$
v_{o}(t)=v_{o}(0)+\square \int_{0}^{t} v_{i}(\tau) d \tau
$$

b) Consider $v_{\mathrm{i}}(t)$ plotted below. Its value changes every T seconds.


[^0]
[^0]:    Suppose $\mathrm{v}_{\mathrm{o}}(0)=0$ and $T=R C$. Plot $\mathrm{v}_{\mathrm{o}}(\mathrm{t})$

